https://ieeexplore.ieee.org/document/6998768/keywords#keywords

Iterative block DFE (IBDFE) and IC techniques based chip-level multiple access

systems

Salah A. Al-iesawi; Mohammed Sh. Ahmed; Tariq M. Salman

Abstract:

In wireless broadband communications using single-carrier interleave division

multiple access (SC-IDMA) with frequency domain equalization (FDE), a new

combination of iterative block decision feedback equalization (IBDFE) and

interference cancellation (IC) techniques is proposed to deal with several users

accessing the common channel. Filtering operations of IBDFE, which are used to

remove intersymbol interference (ISI), are implemented by fast Fourier transforms

(FFTs) that yield a much lower complexity than equivalent time domain (TD)

structures. Further, the proposed structure is performed iteratively and IBDFE is

designed according to the minimum mean square error (MMSE) criteria. The

feedback signal after error-correction code is directly designed from soft detection

at the previous iteration. Simulations performed for downlink communication on

wireless fading multipath channels show that the combination of IBDF-IC provides

an efficient solution with good performance for IDMA systems in ISI channels.

Further, IBDFE-IDMA outperforms existing SC-FDE and multi-carrier IDMA

schemes with cyclic prefixing (CP) and zero padding (ZP) techniques.

Published in: 2013 International Conference on Electrical Communication,

Computer, Power, and Control Engineering (ICECCPCE)

Date of Conference: 17-18 Dec. 2013

Date Added to IEEE *Xplore*: 29 December 2014

ISBN Information:

INSPEC Accession Number: 14837984

DOI: <u>10.1109/ICECCPCE.2013.6998768</u>

Publisher: IEEE

Conference Location: Mosul, Iraq

Keywords: Decoding, Error correction codes, Integrated circuits, Matched filters,

Complexity theory